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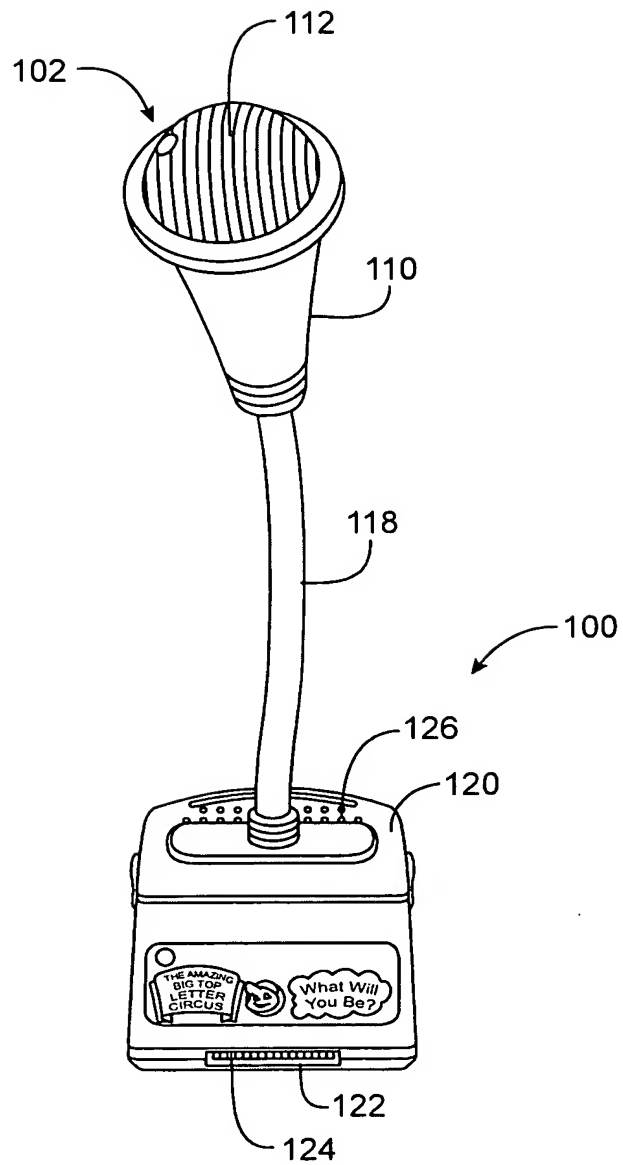


FIG. 1(a)

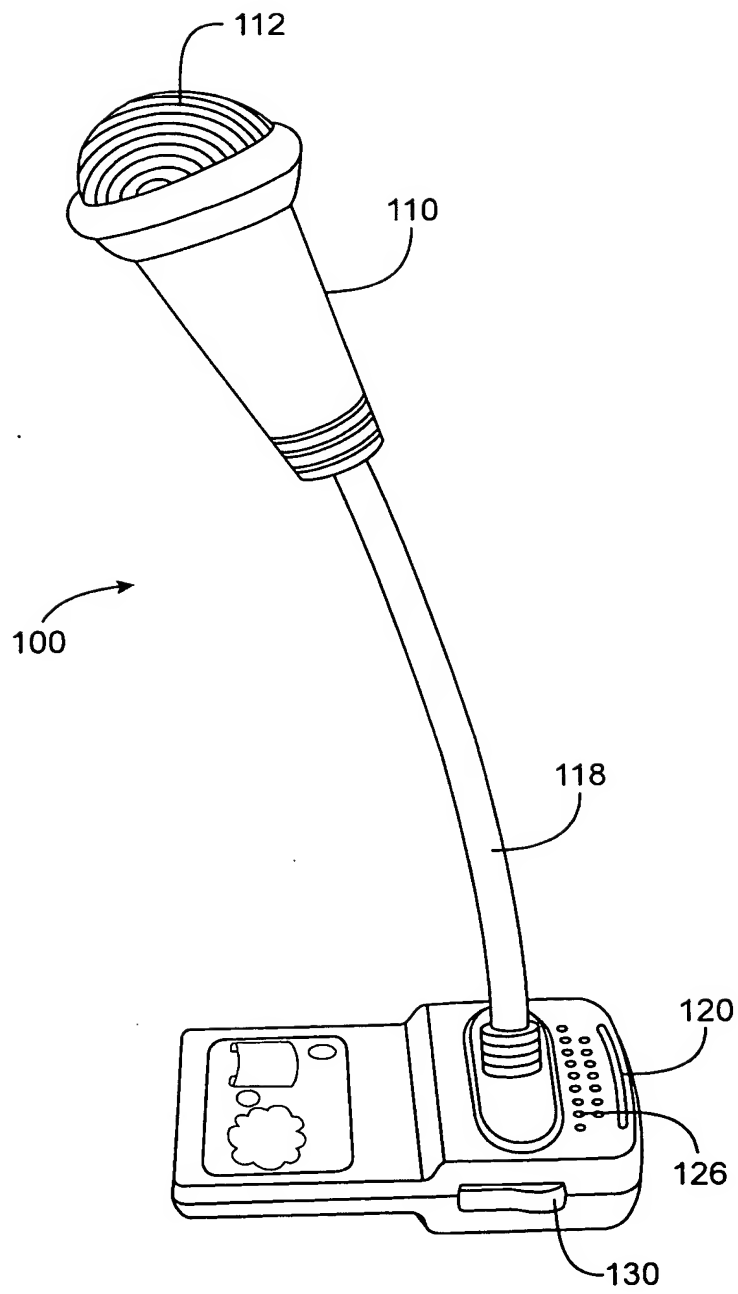


FIG. 1(b)



FIG. 1(c)

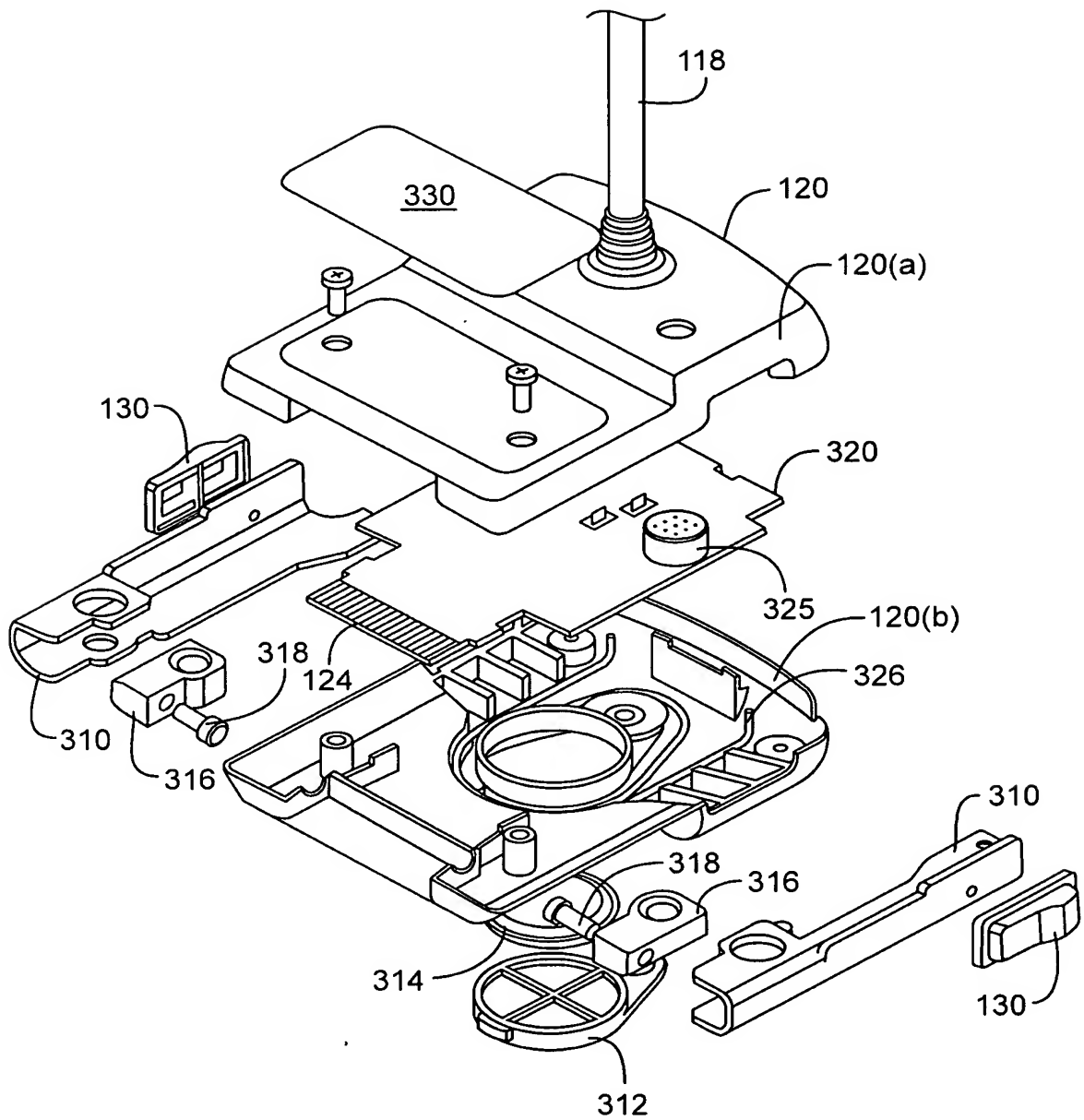


FIG. 2(a)

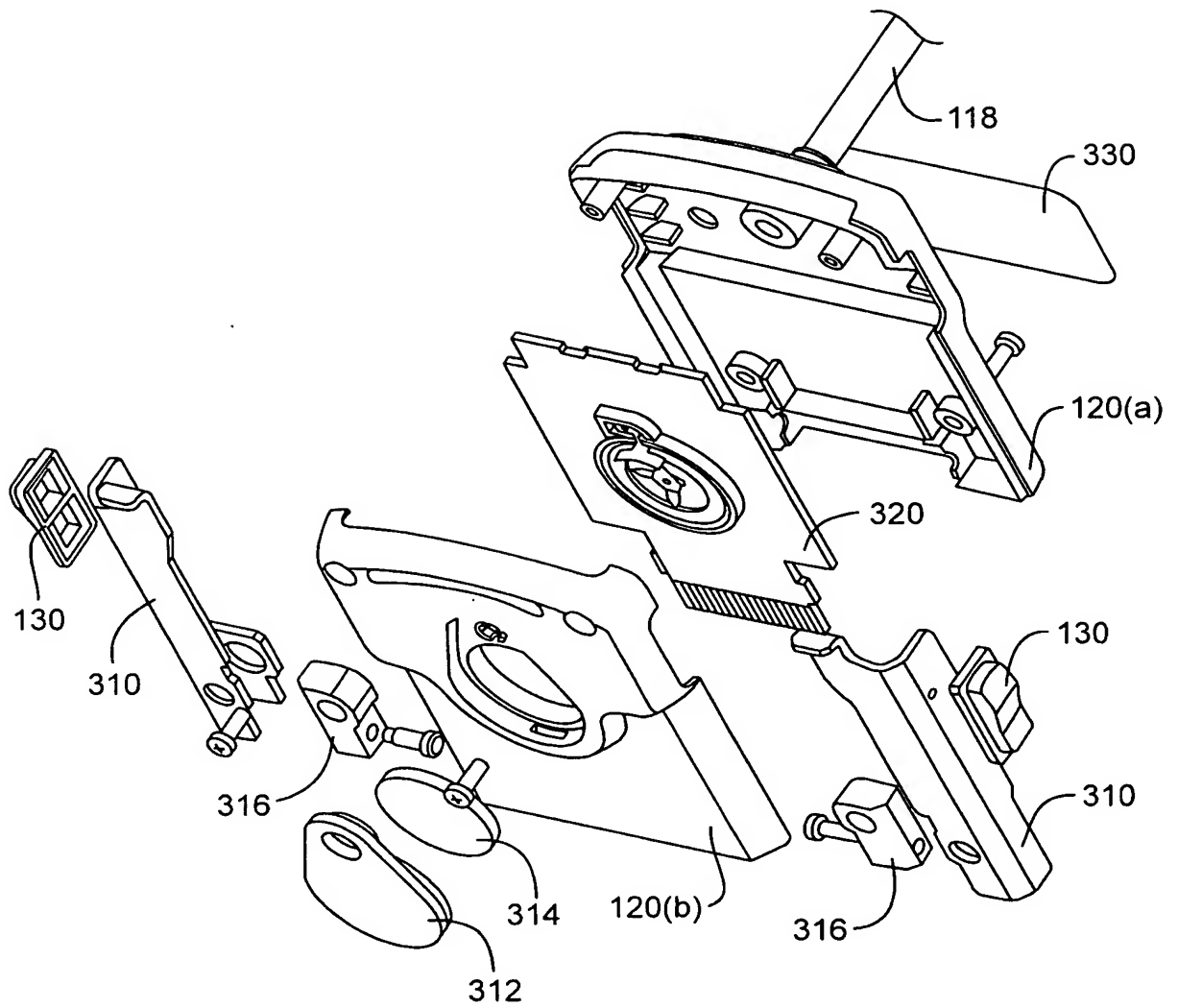


FIG. 2(b)

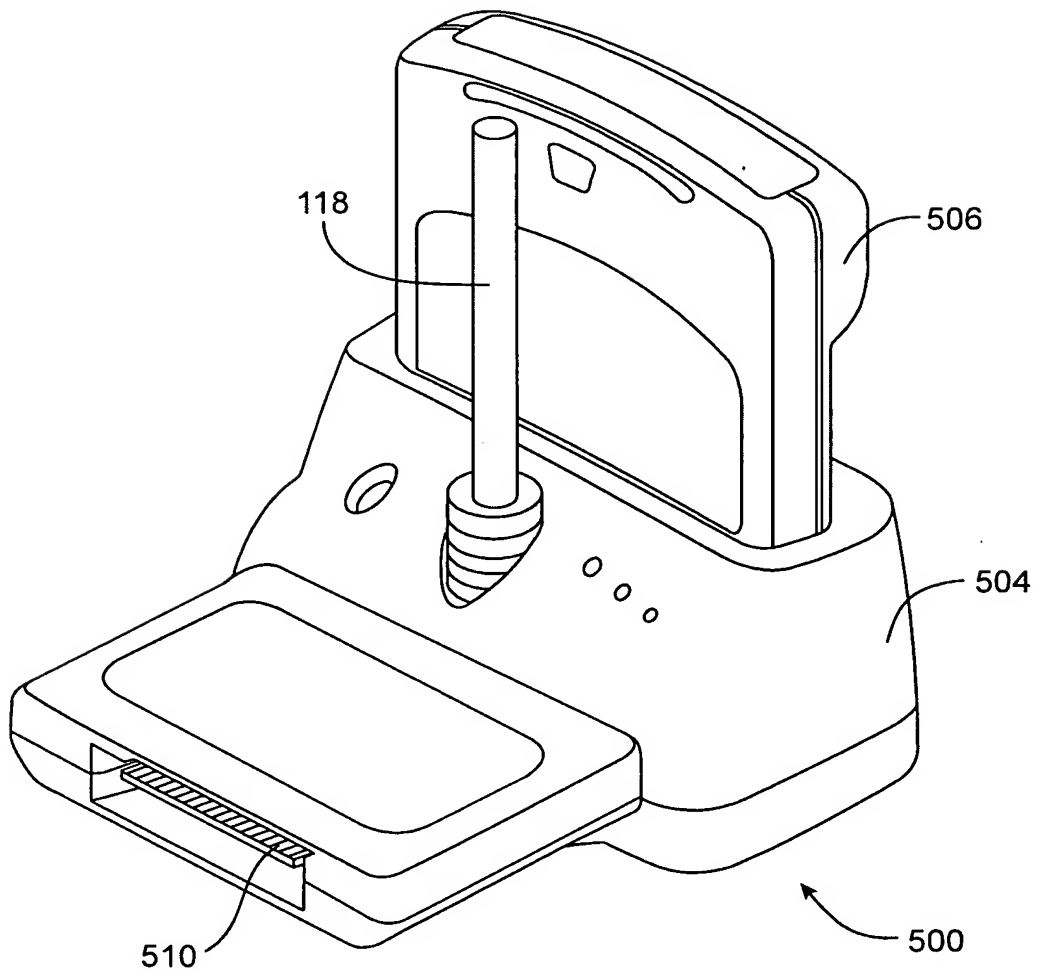


FIG. 3

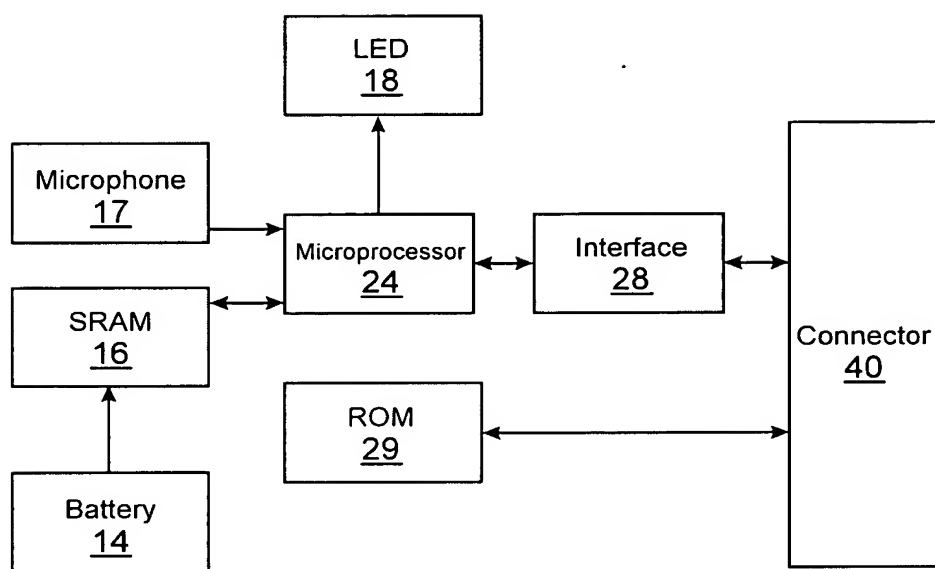


FIG. 4

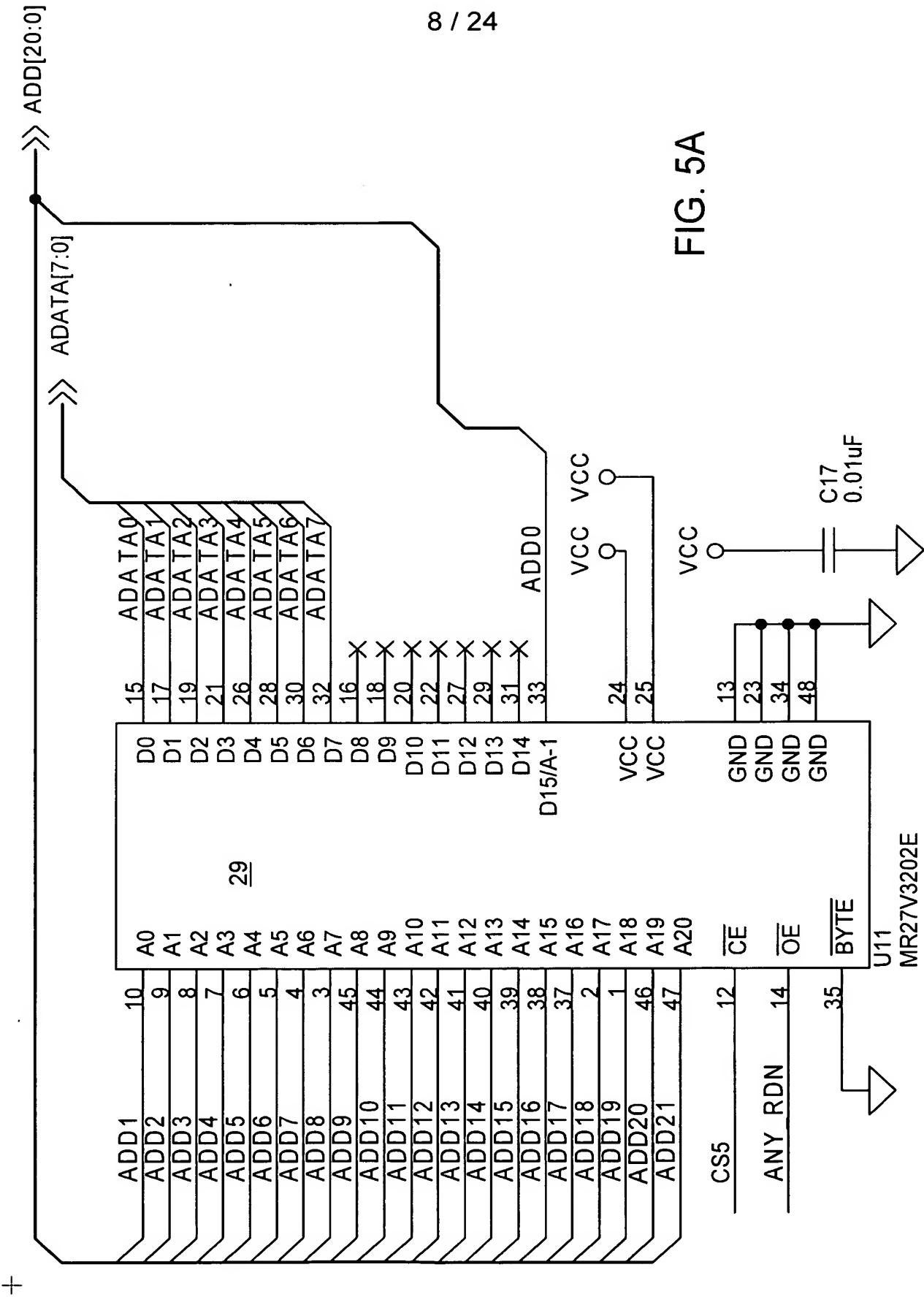


FIG. 5A

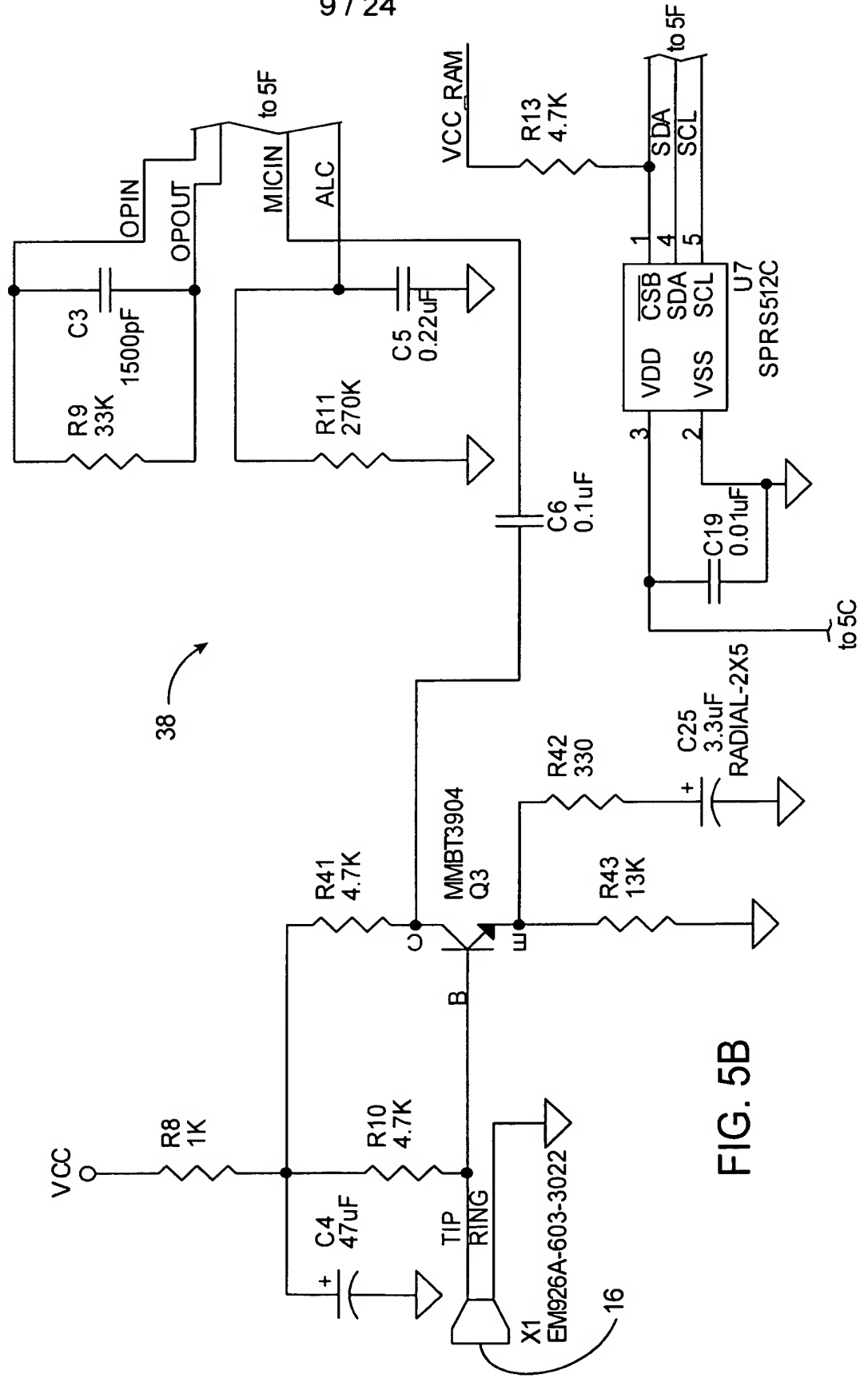


FIG. 5B

38

+

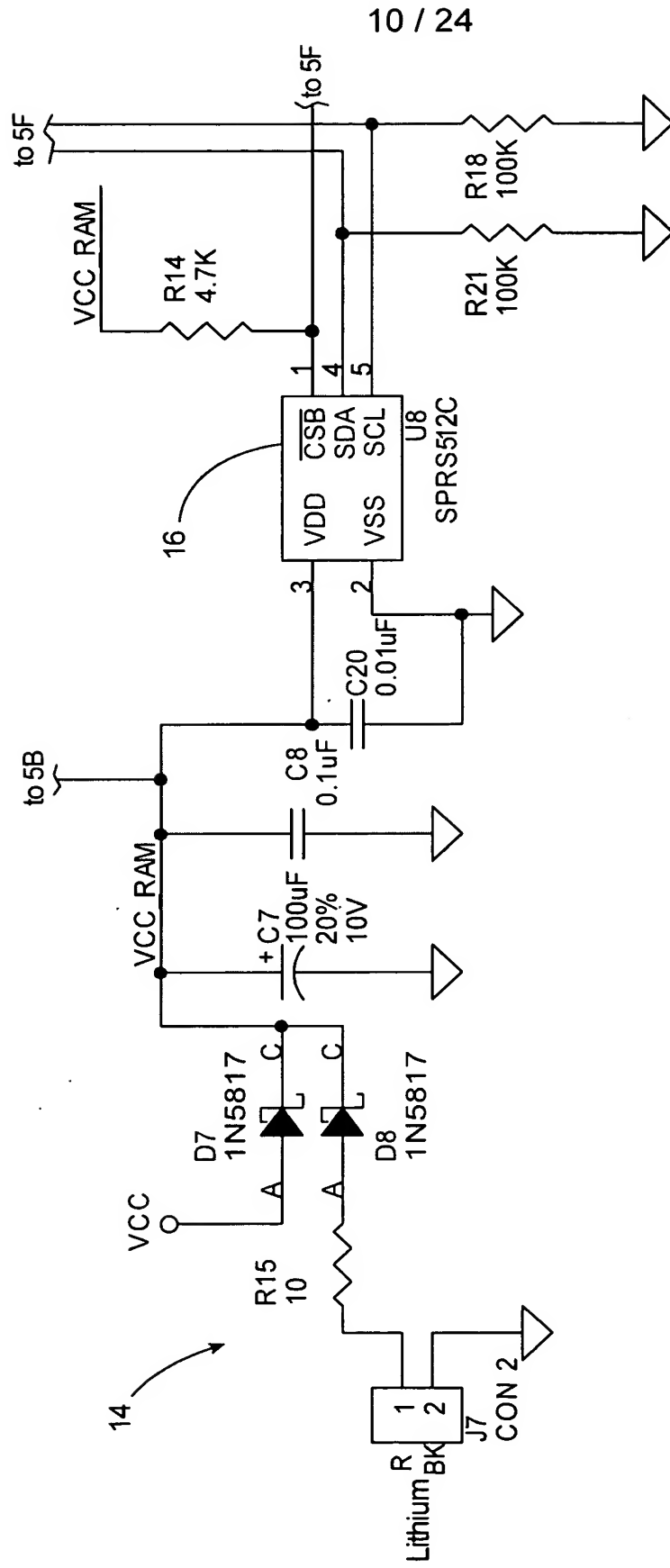


FIG. 5C

+

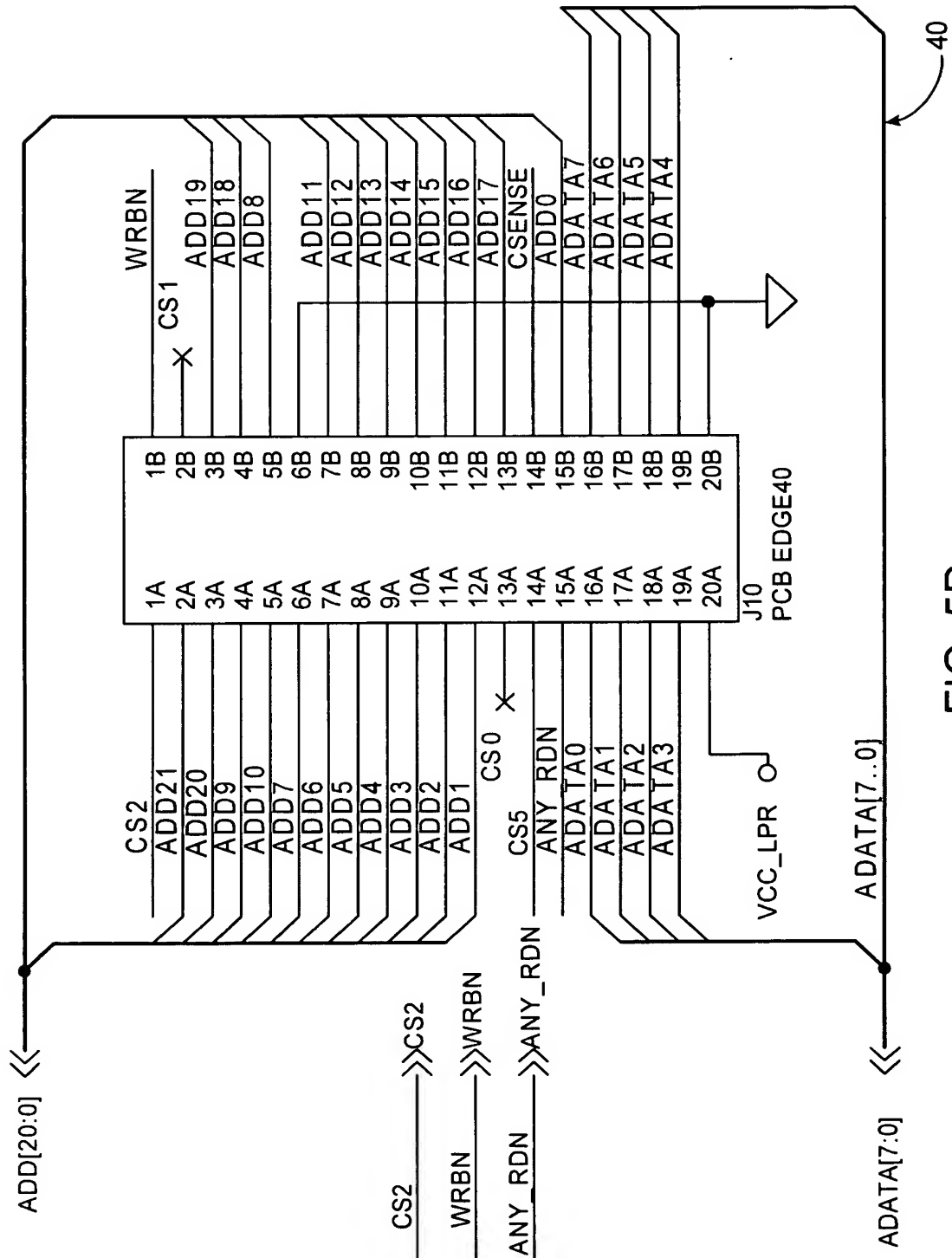


FIG. 5D

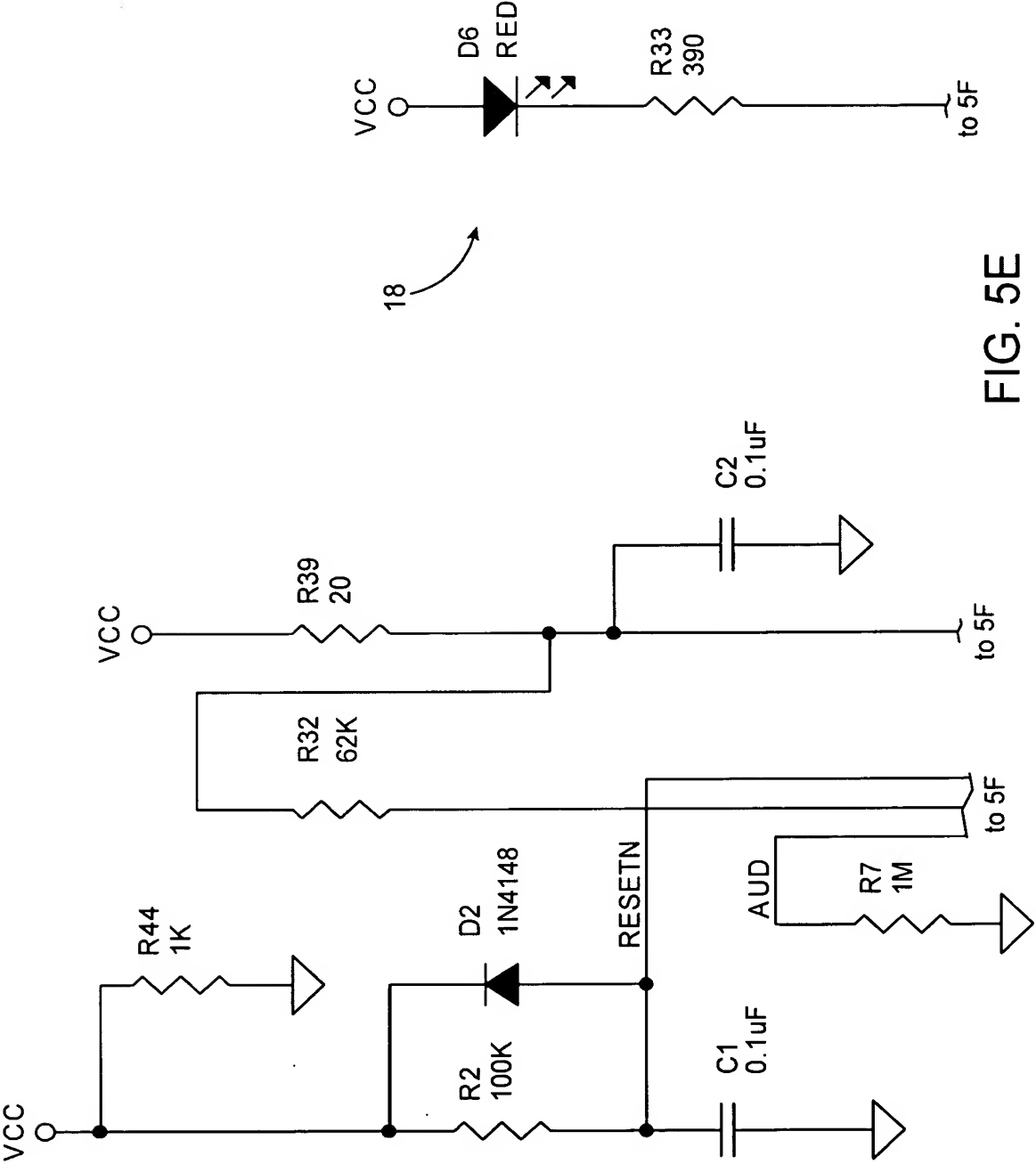


FIG. 5E

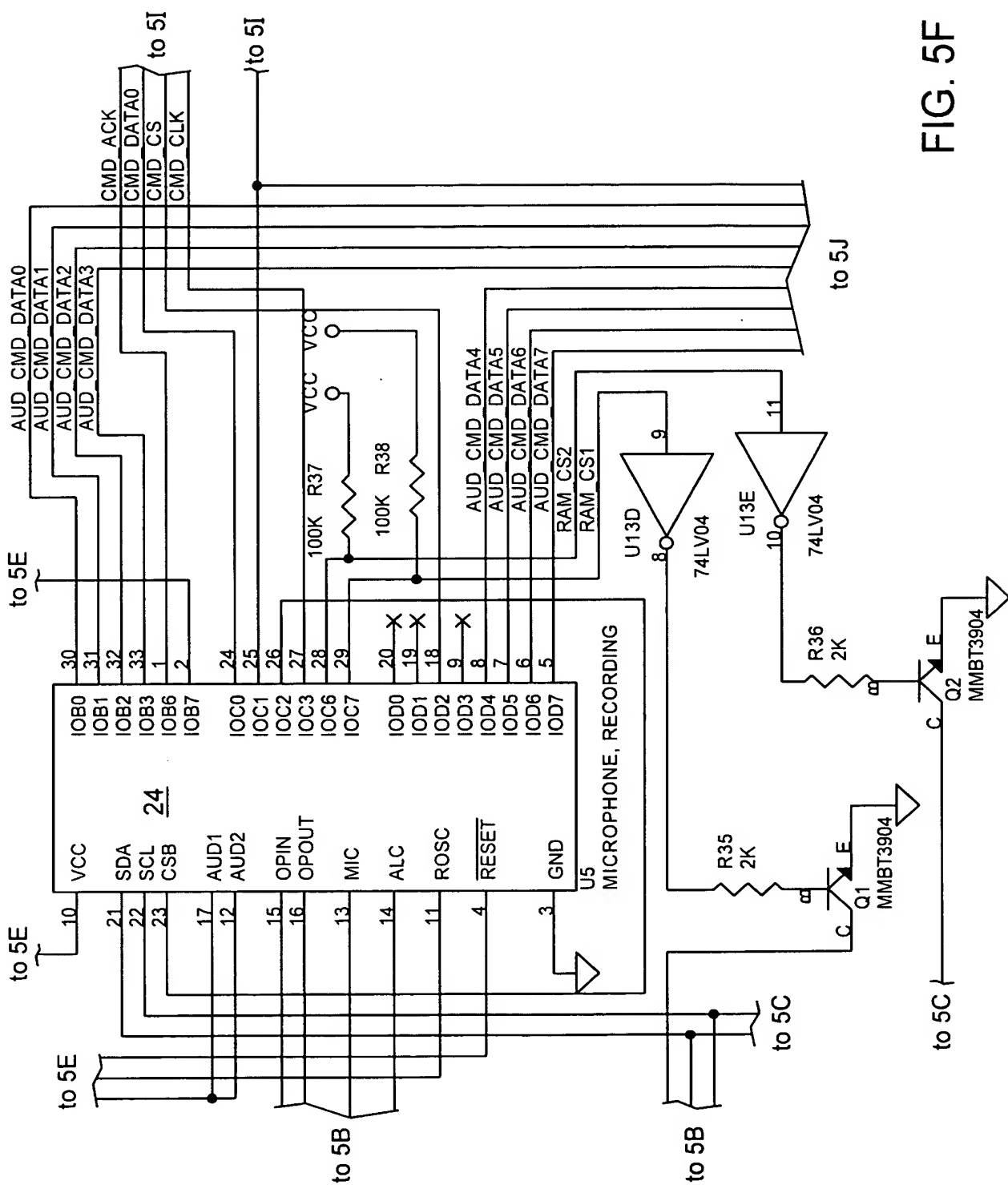


FIG. 5F

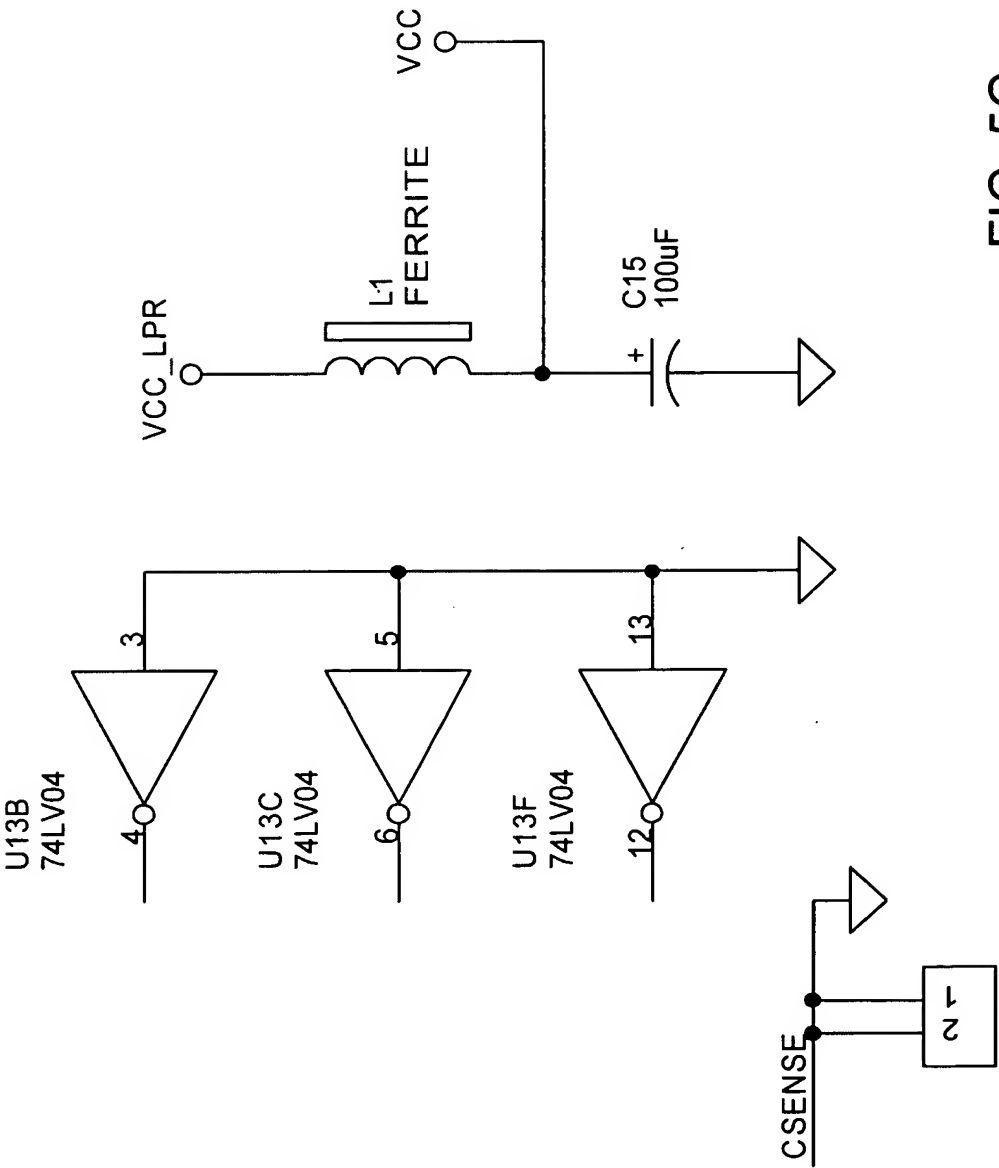
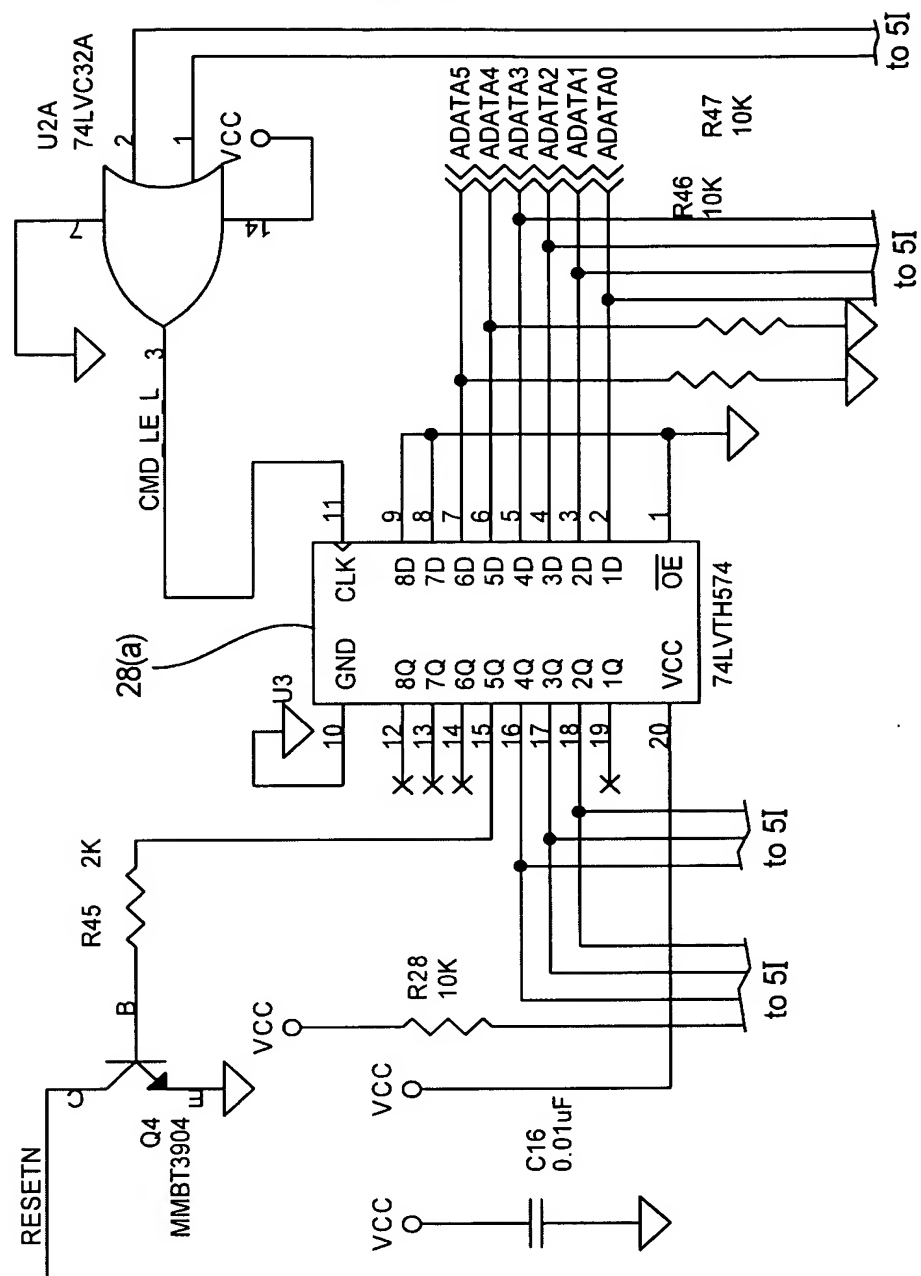


FIG. 5G



INPUTS		OUTPUT Q
OE/	CLK D	
L	^	H
L	^	L
L	HorL	X
H	X	X

74LVTH574

FIG. 5H

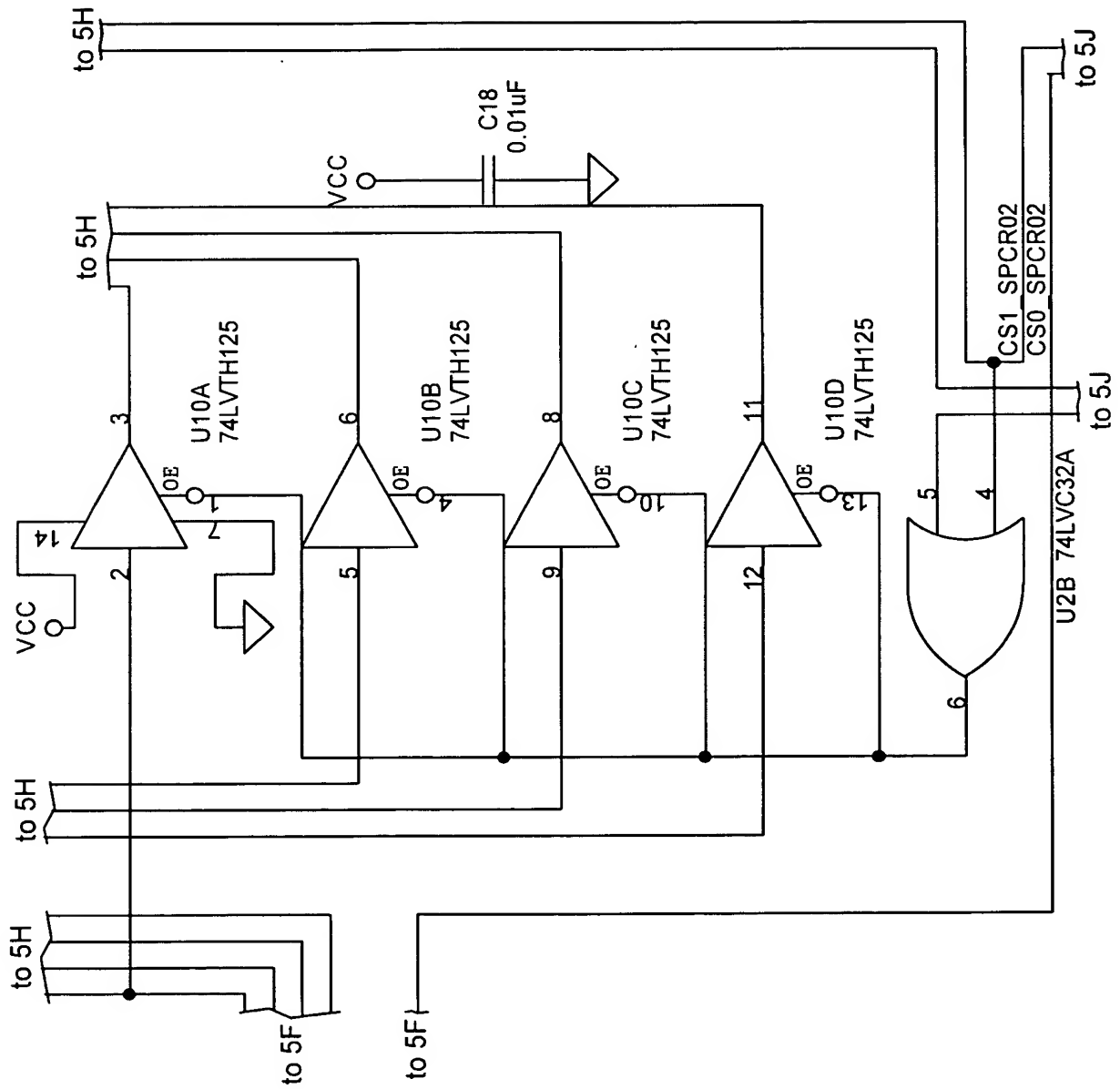


FIG. 5I



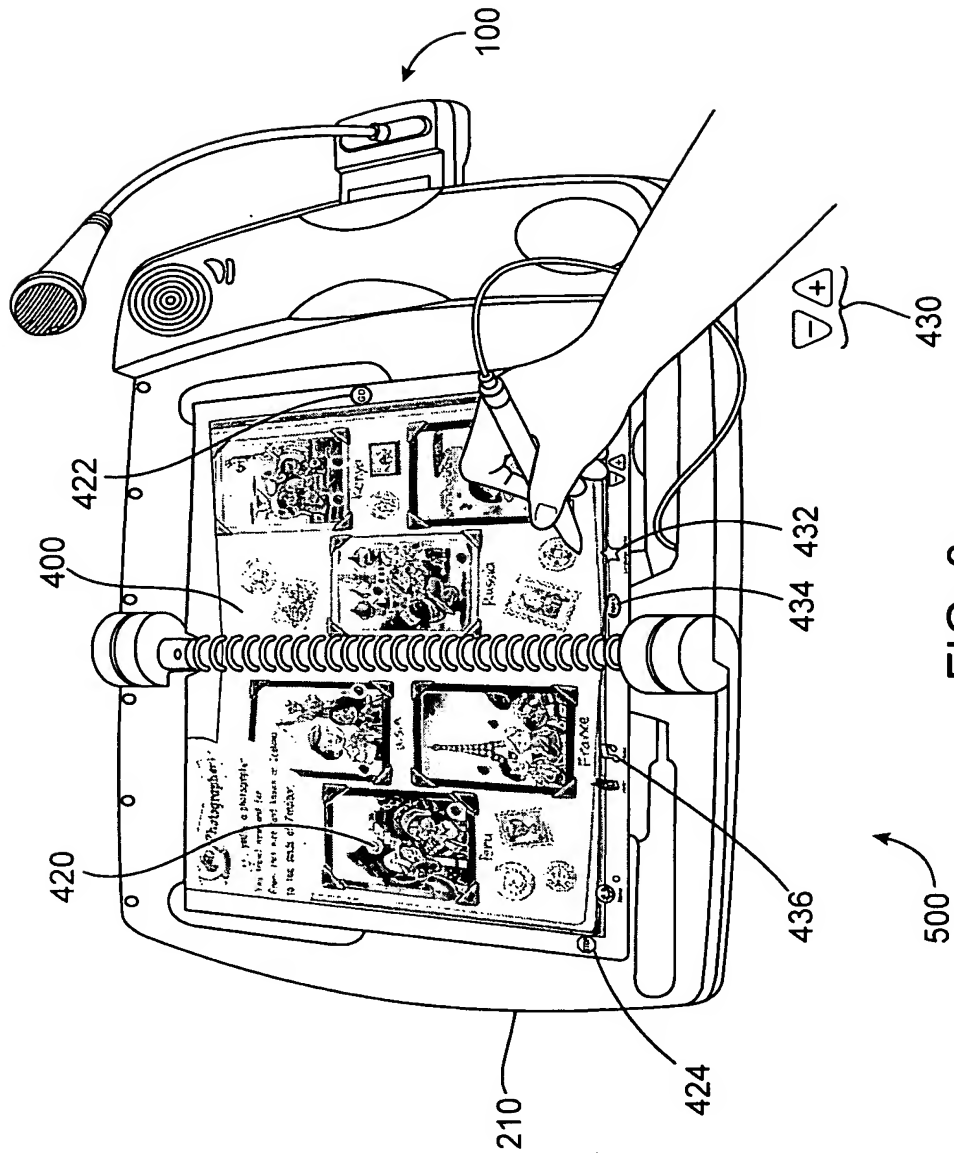


FIG. 6

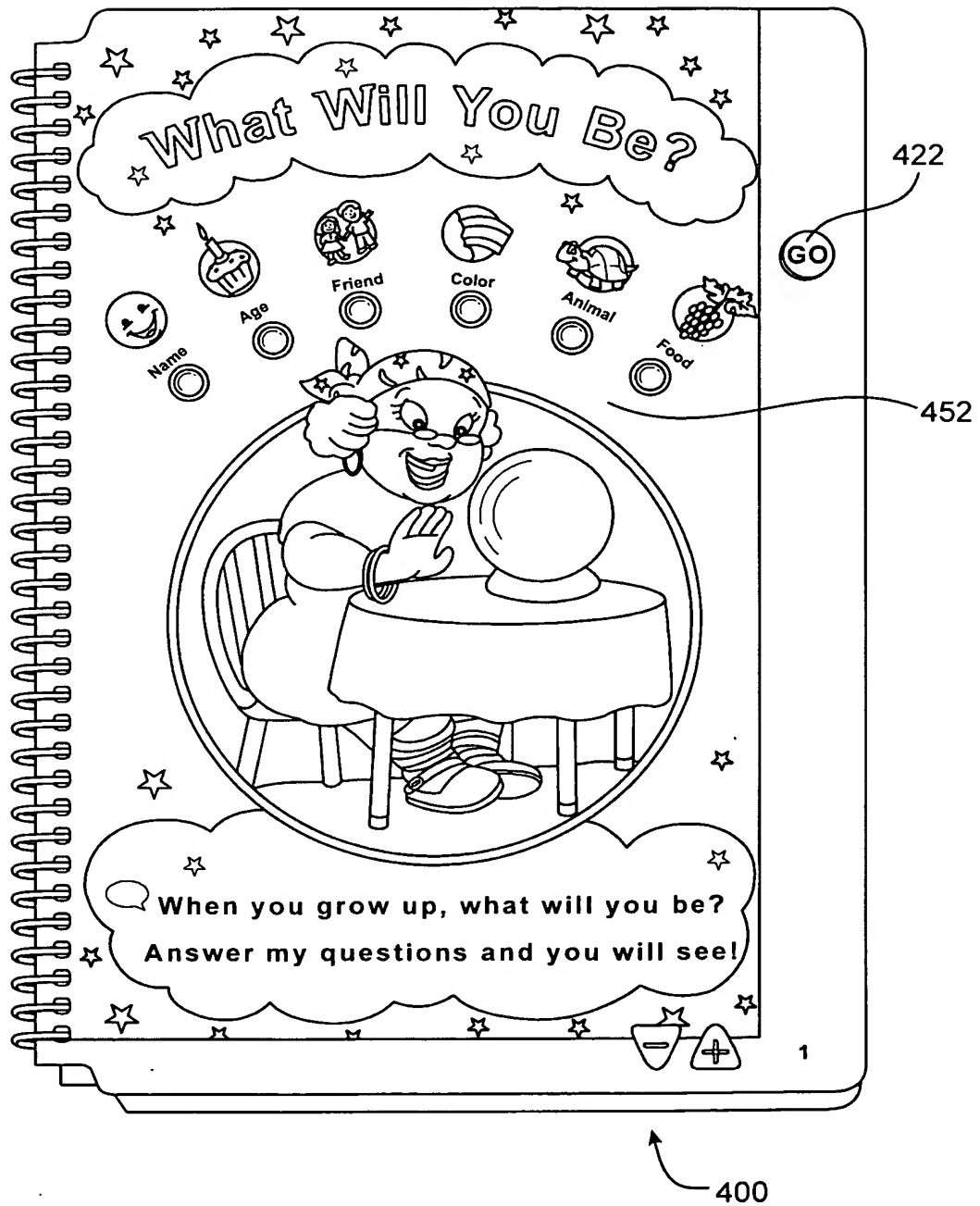


FIG. 7

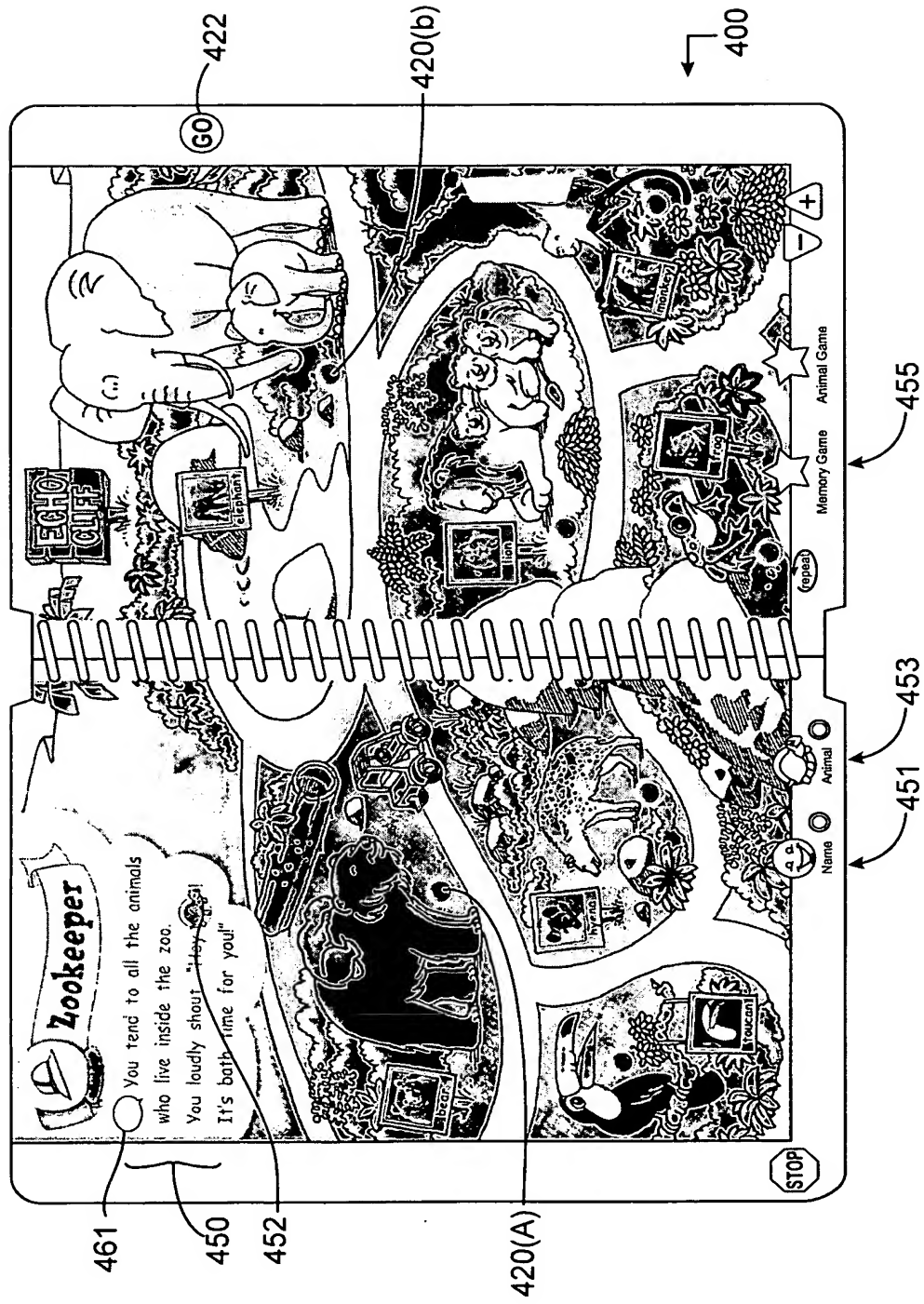


FIG. 8(a)

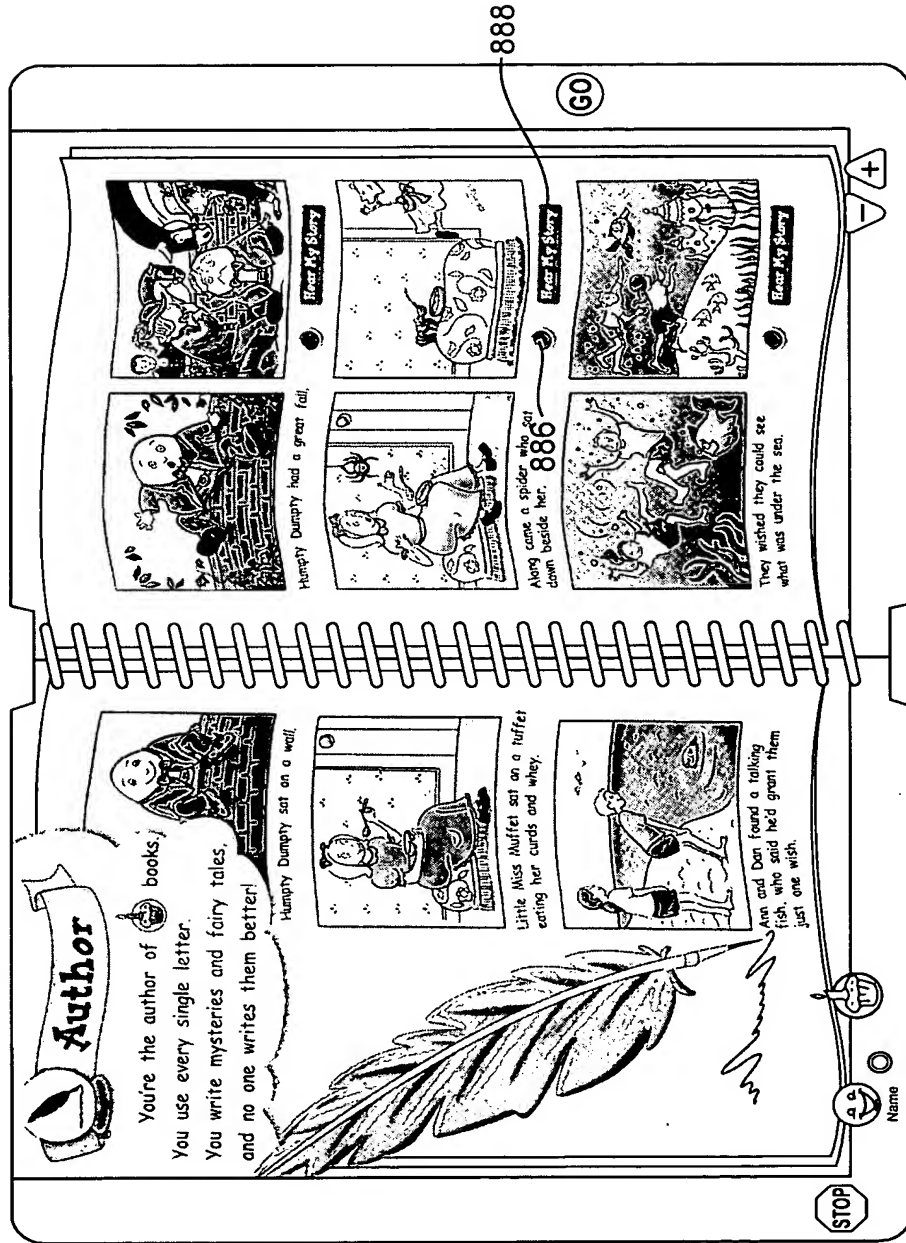


FIG. 8(b)

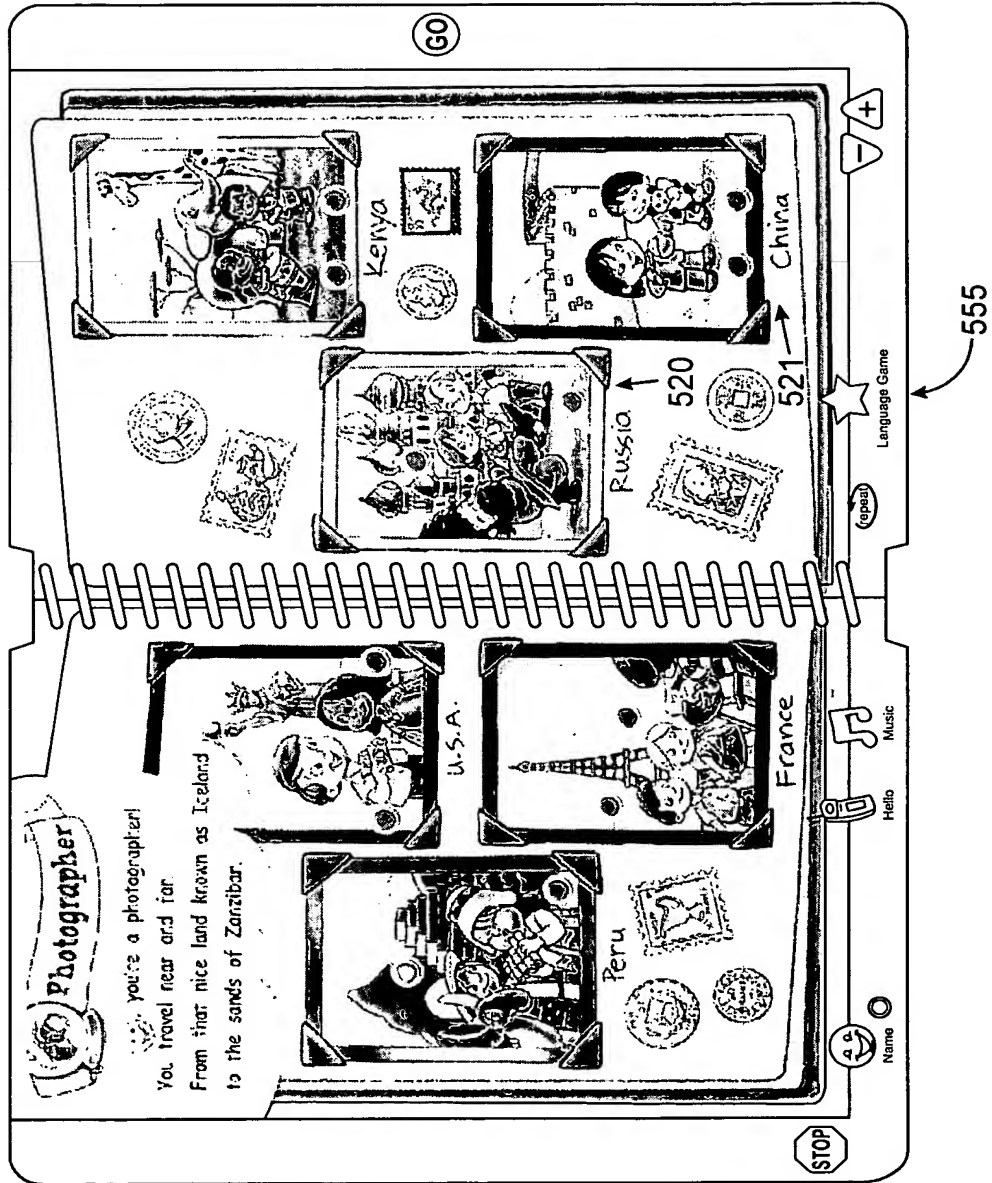


FIG. 8(c)

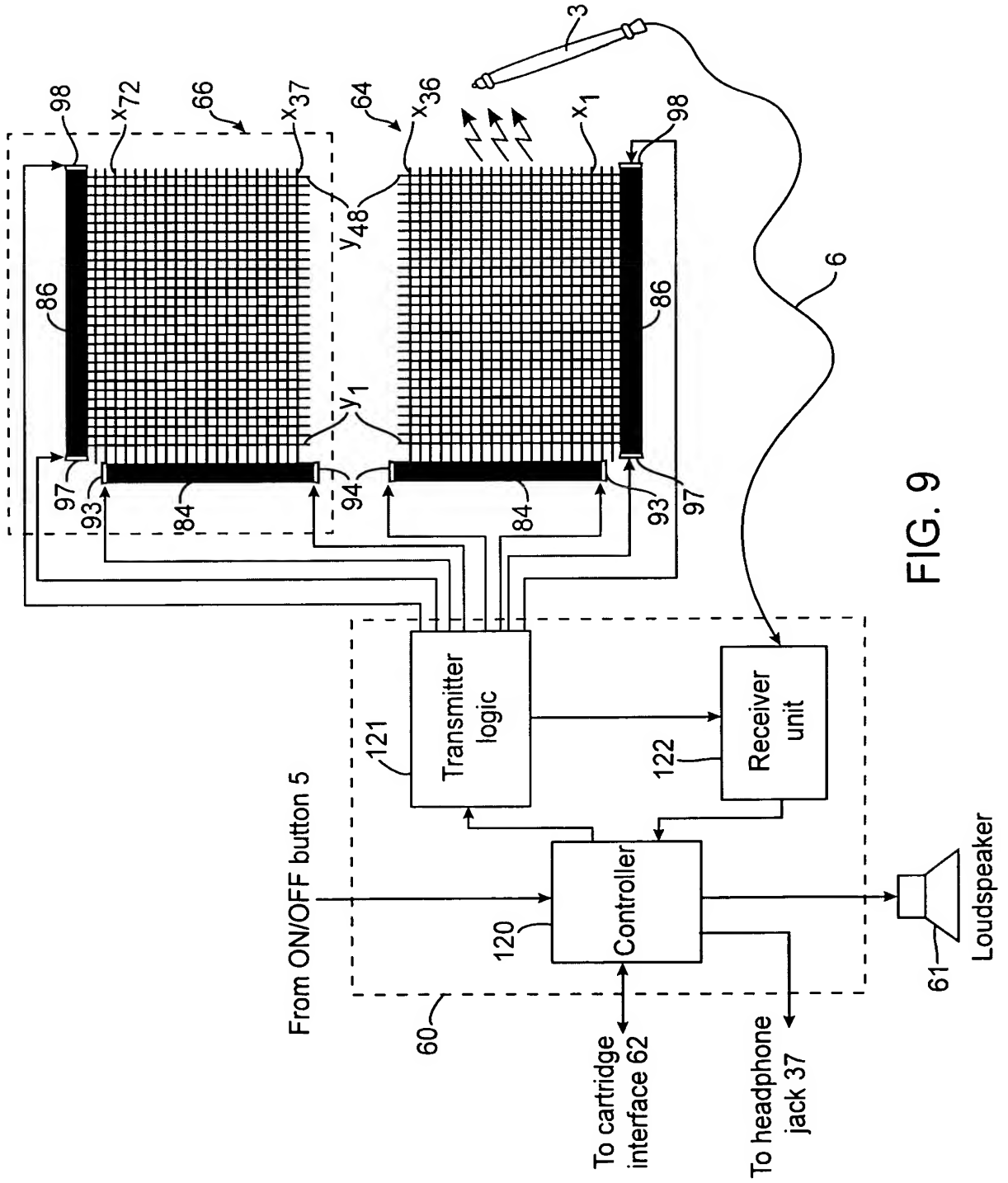


FIG. 9

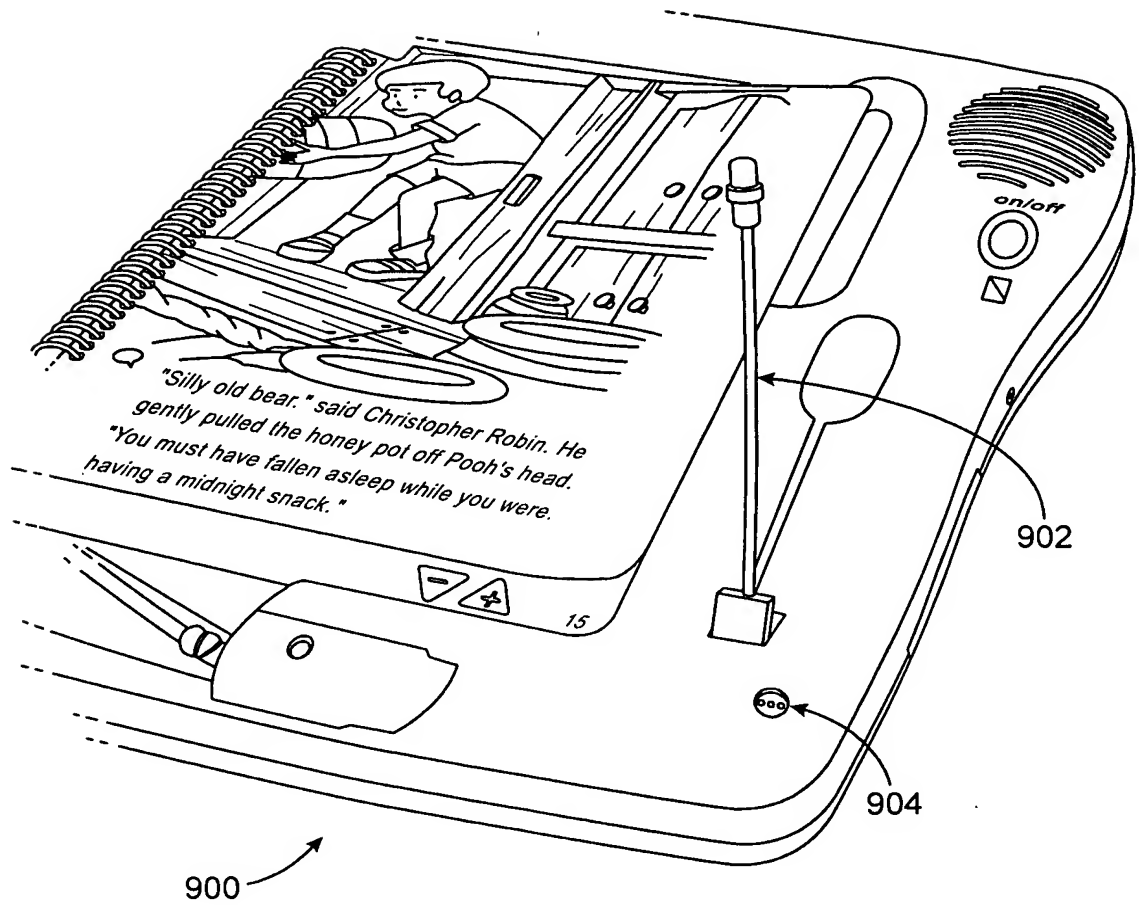


FIG. 10